

BL8593

P-Channel MOSFET with 0.12V Schottky Diode

DESCRIPTION

BL8593 is designed for battery charging controller, which features P-channel MOSFET characteristics and a 0.12V Schottky diode for reverse current blocking. Such reverse current blocking feature cut off the current when source voltage is removed, or lower than drain voltage, no matter the gate voltage indicating the P-MOSFET on or off.

BL8593 is also suitable for high side switch in a system with multi power supplies, when isolating different power supplies becomes essential.

BL8593 can block reverse voltage as high as 10V. So it is safe enough for mobile phone system or other portable device powered by 1 cell Li-ion battery.

BL8593 is available in DFN2x2-6L (2 type of PIN configuration), SC70-5 and DFN1x1-5. Especially with the package DFN1x1-5, BL8593 make itself the smallest package available in the world.

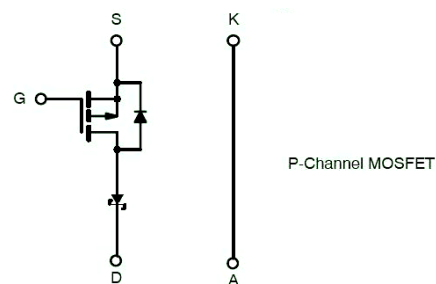
FEATURES

- PMOSFET with SBD for reverse current blocking
- 0.12V Schottky diode forward voltage
- Range of operation input voltage: Max 12V
- Charging current up to 650mA
- Environment Temperature: -20°C~85°C

APPLICATIONS

- Cell phone and other portable device

FUNCTION DIAGRAM



ORDERING INFORMATION / PIN CONFIGURATION / MARKING

BL8593CKCTR DFN2x2-6L (compatible to DFN2x3 pin out)	BL8593CBKCTR DFN2x2-6L (compatible to DFN2x2 pin out)	BL8593CA5TR SC70-5	BL8593CKDTR DFN1x1-5L

Notice: YW means the year and week parts being manufactured, subjected to change. OA is the code of the product, it will not be changed on any part.

ABSOLUTE MAXIMUM RATING

Parameter	Symbol	5 sec	Steady State	Unit
Forward Voltage(Source-Drain)	V_{SD}	12		V
Gate-Source Voltage (MOSFET)	V_{GS}	-8~+0.3	-8~+0.3	
Continuous Drain Current	I_D	0.8	0.5	mA
Pulsed Drain Current (MOSFET)	I_{DM}	1		A
Maximum Power Dissipation	P_D	1	0.5	W
Operating Junction Temperature Range	T_J	-20 to 125		°C
Storage Temperature Range	T_{stg}	-40 to 150		
Soldering Recommendations (Peak Temperature)		260°C, 10s		

THERMAL RESISTANCE RATING

Parameter		Device	Symbol	Typical	Maximum	Unit
junction-to-Ambient	t ≤ 5 sec	DFN2x2	R_{thJA}	50	60	°C /W
		DFN1x1		77	95	
		SC70-5		250	280	
	Steady State	DFN2x2		105	120	
		DFN1x1		160	200	
		SC70-5		330	400	
Junction to Case	Steady State	DFN2x2	R_{thJC}	20	30	
		DFN1x1		33	40	
		SC70-5		150	175	

ELECTRICAL CHARACTERISTICS

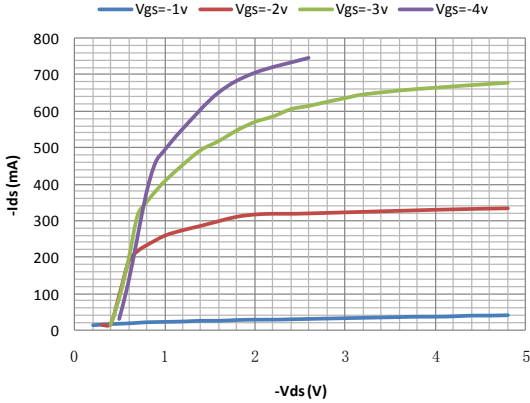
$T_J=25^\circ\text{C}$

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{th}	Threshold Voltage	$I_{ds}=-10\mu\text{A}$, $V_{ds}=V_{gs}$	-1.0	-0.7	-0.4	V
I_{gs}	Gate-Source Leakage current	$V_{gs}=8\text{V}$	0	12	20	uA
I_{Dss1}	PMOS off-state leakage	$V_{gs}=0$, $V_s=9\text{V}$, $V_d=0\text{V}$		0.5	5	uA
I_{Dss2}	PMOS reverse block leakage	$V_g=0$, $V_s=0\text{V}$, $V_d=4.5\text{V}$		2	10	uA
I_{dson}	On -state drain current	$V_s=5\text{V}$, $V_g=1\text{V}$, $V_d=4\text{V}$	-800	-650	-500	mA
R_{dson}	V_{ds}/I_{dson}	$V_s=5\text{V}$, $V_g=1\text{V}$, $V_d=4\text{V}$	1.25	1.5	2	ohm
V_{fsbd}	Forward voltage of schottky	$V_s=4\text{V}$, $V_g=0\text{V}$, $I_{ds}=0$,	0.08	0.12	0.16	V

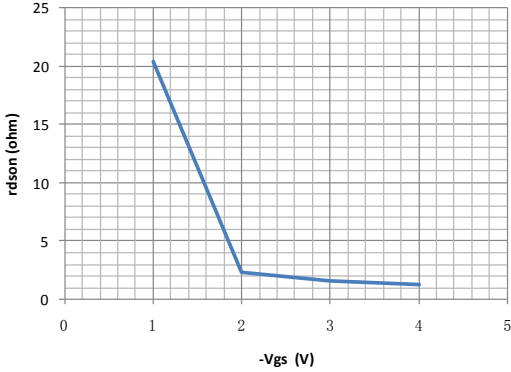
TYPICAL PERFORMANCE CHARACTERISTICS

T=25°C unless specified.

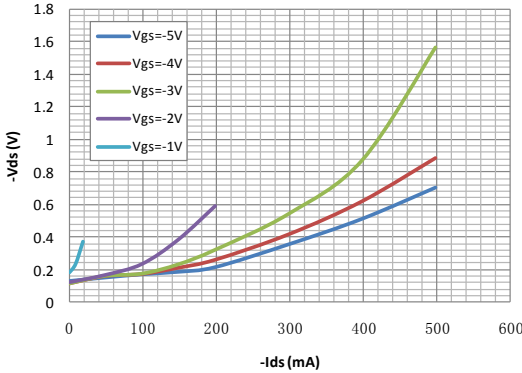
Output Characteristics



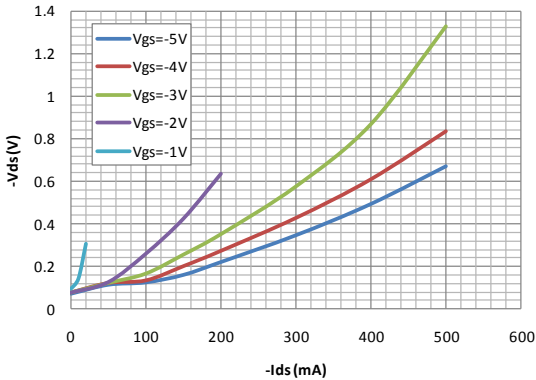
On Resistance Vs Vgs



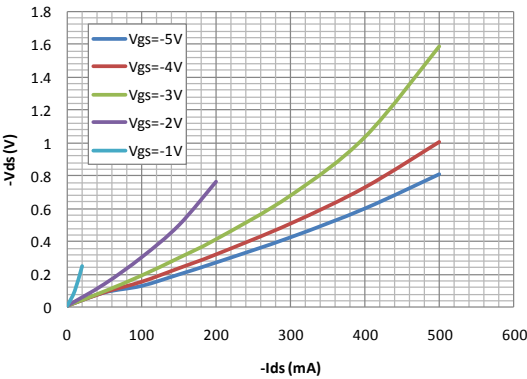
Dropout Voltage (Vds) Vs. Charge current (Ids), T = 25°C



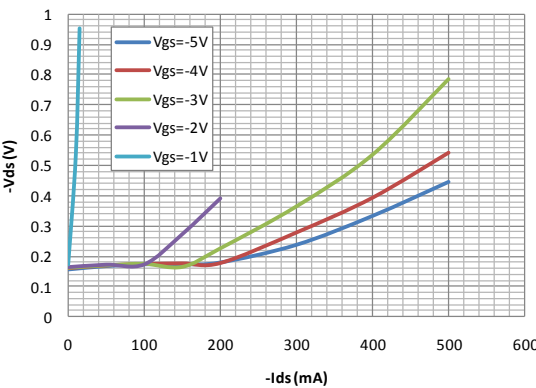
Dropout Voltage (Vds) Vs. Charge current (Ids), T = 80°C



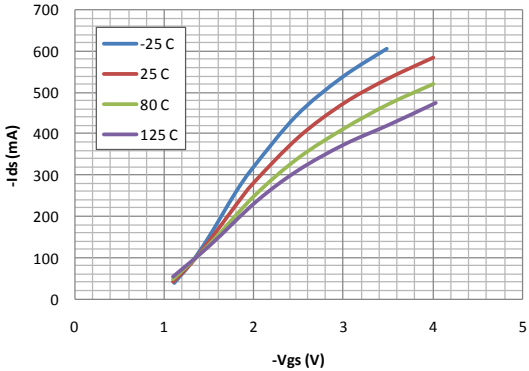
Dropout Voltage (Vds) Vs. Charge current (Ids), T = 125°C



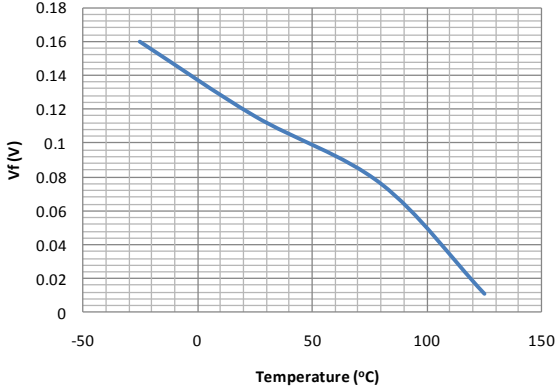
Dropout Voltage (Vds) Vs. Charge current (Ids), T = -25°C



Charge Current (Ids) Vs. Gate Voltage

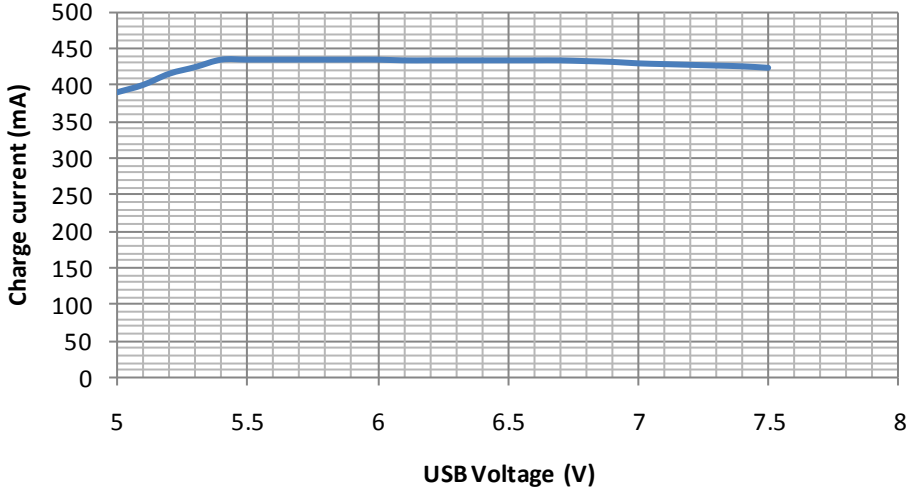


Schottky Diode Forward Voltage

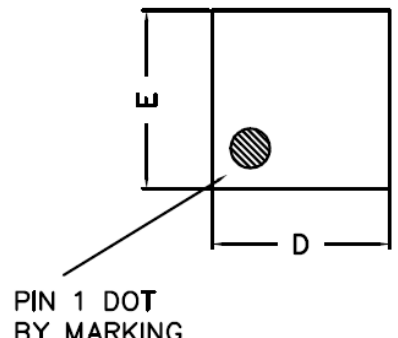
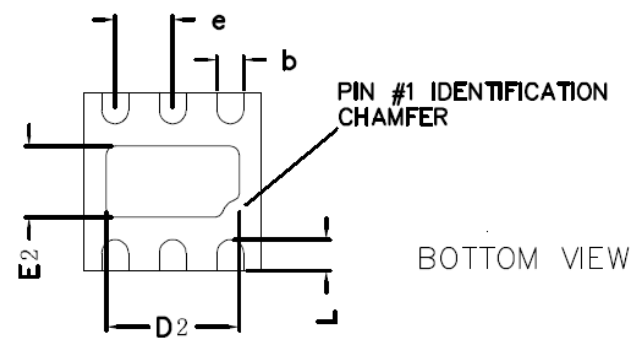
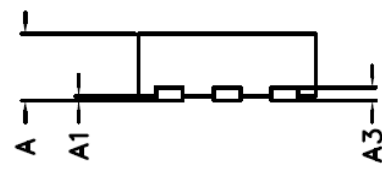


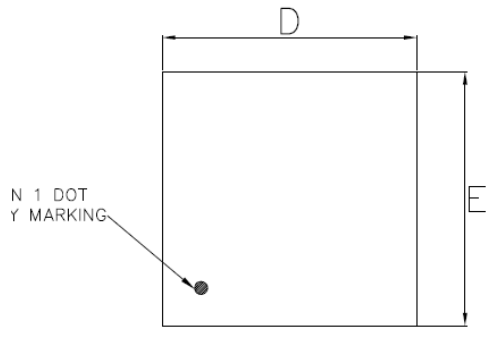
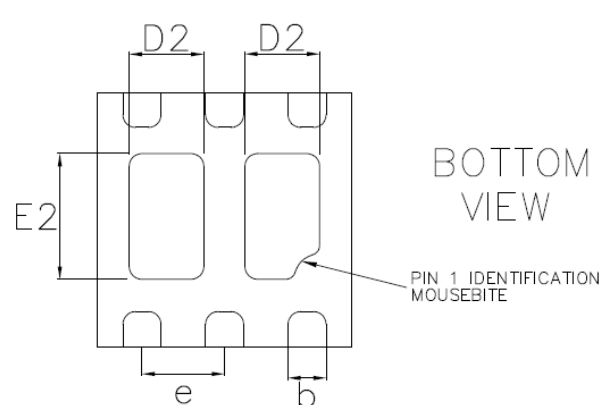
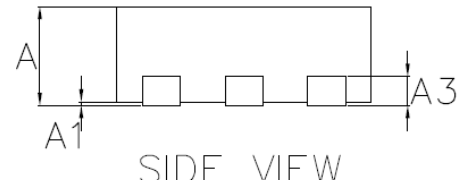
Charge current Vs USB voltage tested on actual cell phone powered by MTK chipset

Icharge



PACKAGE OUTLINE

Package	DFN2x2-6 single pad	Devices per reel	3000	Unit	mm																																																			
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Package	SC70-5	Devices per reel	3000	Unit	mm
Package specification:					